

Search Results

01/15/2002

Search Results for: [(((result* <or> operand*) <near> bypass*) <near> (compiler-driven <or> program-driven <or> compiler driven <or> program driven))]
 Found 4 of 87,654 searched. → Rerun within the Portal

Search within Results

 > Advanced Search


> Search Help/Tips

Sort by: Title Publication Publication Date Score ☒ Binder

Results 1 - 4 of 4 short listing

1 Lightweight remote procedure call


20%

 Brian N. Bershad , Thomas E. Anderson , Edward D. Lazowska , Henry M. Levy
 ACM Transactions on Computer Systems (TOCS) February 1990
 Volume 8 Issue 1

Lightweight Remote Procedure Call (LRPC) is a communication facility designed and optimized for communication between protection domains on the same machine. In contemporary small-kernel operating systems, existing RPC systems incur an unnecessarily high cost when used for the type of communication that predominates between protection domains on the same machine. This cost leads system designers to coalesce weakly related subsystems into the same protection domain, trading safety for ...

2 Lightweight remote procedure call

18%

 B. Bershad , T. Anderson , E. Lazowska , H. Levy
 ACM SIGOPS Operating Systems Review , Proceedings of the Twelfth ACM symposium on Operating systems principles November 1989
 Volume 23 Issue 5

Lightweight Remote Procedure Call (LRPC) is a communication facility designed and optimized for communication between protection domains on the same machine. In contemporary small-kernel operating systems, existing RPC systems incur an unnecessarily high cost when used for the type of communication that predominates between protection domains on the same machine. This cost leads system designers to coalesce weakly-related subsystems into the same protection domain, trading ...


3 Better exploration of region-level value locality with integrated computation reuse and value prediction

2%

 Youfeng Wu , Dong-Yuan Chen , Jesse Fang
 the 28th annual international symposium on Computer architecture June 2001

4 Lx

1%

 Paolo Faraboschi , Geoffrey Brown , Joseph A. Fisher , Giuseppe Desoli , Fred Homewood
 ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture May 2000
 Volume 28 Issue 2

Lx is a scalable and customizable VLIW processor technology platform designed by Hewlett-Packard and STMicroelectronics that allows variations in instruction issue width, the number and capabilities of structures and the processor instruction set. For Lx we developed the architecture and software from the beginning to support both scalability (variable numbers of identical processing resources) and customizability (special purpose resources). In this paper we consider the followi ...

Results 1 - 4 of 4 **short listing**

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2001 ACM, Inc.